# 1. Part No. Expression

# PDB 1608 1R0 M Z F

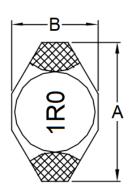
- (a)
- (b)
- (c) (d) (e) (f)
- (a) Series Code

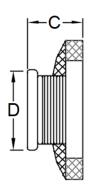
(d) Tolerance Code

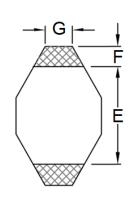
(b) Dimension Code

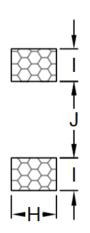
- (e) Special Code
- (c) Inductance Code
- (f) Packaging Code

# 2. Configuration & Dimensions (Unit: mm)









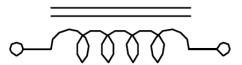
Recommended PCB Layout

Note: 1. The above PCB layout reference only.

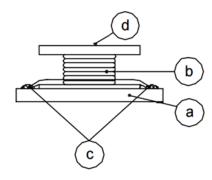
2. Marking: Inductance Code

А	В	С	D	E
6.60 Max	4.45 Max	2.92 Max	3.90 Ref	4.32 Ref
F	G	Н	1	J
1.02 Ref	1.27 Ref	3.56 Ref	1.40 Ref	4.06 Ref

### 3. Schematic



## 4. Material List



- (a) Base
- (b) Wire
- (c) Epoxy
- (d) Marking

# 5. General Specifications

- (a) Operating Temp.: -40°C to +125°C (including self-temperature rise)
- (b) Storage Temperature: -40°C to +125°C (On board)
- (c) All test data referenced to 25°C ambient.
- (d) Heat Rated Current (Irms) will cause the coil temperature rise ΔT of 40°C Max.
- (e) Saturation Current (Isat) will cause inductance L0 to drop approximately 10%.
- (f) Rated Current: The lower value of Isat and Irms.
- (g) Storage Condition (Component in its packaging)
  - i) Temperature: Less than 40°C
  - ii) Humidity: Less than 70% RH

# 6. Electrical Characteristics

Part Number	Inductance (uH) @0A ±20%	Test Frequency	SRF (MHz) Typ	DCR (Ω) Max	Irms (A)	lsat (A)
PDB16081R0MZF	1.0	0.1V/100KHz	130.0	0.05	2.90	2.90
PDB16081R5MZF	1.5	0.1V/100KHz	115.0	0.06	2.80	2.60
PDB16082R2MZF	2.2	0.1V/100KHz	90.0	0.07	2.40	2.30
PDB16083R3MZF	3.3	0.1V/100KHz	70.0	0.08	2.00	2.00
PDB16084R7MZF	4.7	0.1V/100KHz	50.0	0.09	1.50	1.50
PDB1608100MZF	10.0	0.1V/100KHz	35.0	0.16	1.10	1.10
PDB1608150MZF	15.0	0.1V/100KHz	30.0	0.23	1.00	0.90
PDB1608220MZF	22.0	0.1V/100KHz	20.0	0.37	0.80	0.70
PDB1608330MZF	33.0	0.1V/100KHz	15.0	0.51	0.60	0.58
PDB1608470MZF	47.0	0.1V/100KHz	14.0	0.64	0.50	0.50
PDB1608680MZF	68.0	0.1V/100KHz	11.0	0.86	0.40	0.40
PDB1608101MZF	100.0	0.1V/100KHz	9.0	1.27	0.30	0.31



# 7. Soldering Specification

Mildly activated rosin fluxes are preferred. Our terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

### 7-1. IR Soldering Reflow

Recommended temperature profiles for lead free re-flow soldering in Figure 1, Table 1.1 & 1.2 (J-STD-020E).

### 7-2. Iron Reflow

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended (Figure 2).

#### Note:

- (a) Preheat circuit and products to 150°C.
- (b) 355°C tip temperature (Max.)
- (c) Never contact the ceramic with the iron tip
- (d) 1.0mm tip diameter (Max.)
- (e) Use a 20 watt soldering iron with tip diameter of 1.0mm
- (f) Limit soldering time to 4~5 sec.

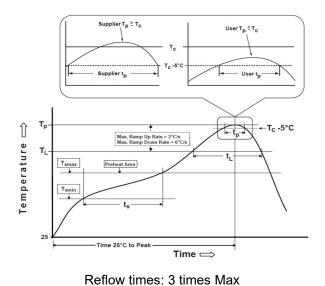
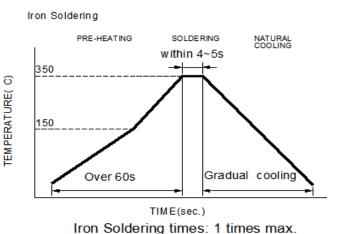


Figure 1: IR Soldering Reflow



Soldering iron method: 350±5°C Max

Figure 2: Iron soldering temperature profiles



Table (1.1) Reflow Profiles

Profile Type:	Pb-Free Assembly
Preheat	
-Temperature Min (T <sub>smin</sub> )	150°C
-Temperature Max (T <sub>smax</sub> )	200°C
-Time $(t_s)$ from $(T_{smin}$ to $T_{smax})$	60-120seconds
Ramp-up rate (T <sub>L</sub> to T <sub>p</sub> )	3°C /second max.
Liquids temperature (T <sub>L</sub> )	217°C
Time (t <sub>L</sub> ) maintained above T <sub>L</sub>	60-150 seconds
Classification temperature (Tc)	See Table (1.2)
Time (t <sub>p</sub> ) at Tc- 5°C (Tp should be equal to or less than Tc.)	*< 30 seconds
Ramp-down rate $(T_p \text{ to } T_L)$	6°C /second max.
Time 25°C to peak temperature	8 minutes max.

**Tp**: maximum peak package body temperature, **Tc**: the classification temperature.

For user (customer) **Tp** should be equal to or less than **Tc**.

Table (1.2) Package Thickness/Volume and Classification Temperature (T<sub>c</sub>)

	Package	Volume mm <sup>3</sup>	Volume mm <sup>3</sup>	Volume
	Thickness	<350	350-2000	mm³ >2000
PB-Free	<1.6mm	260°C	260°C	260°C
	1.6-2.5mm	260°C	250°C	245°C
Assembly	≥2.5mm	250°C	245°C	245°C

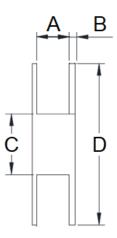
Reflow is referred to standard IPC/JEDEC J-STD-020E.

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<sup>\*</sup>Tolerance for peak profile temperature (Tp) is defined as a supplier minimum and a user maximum.

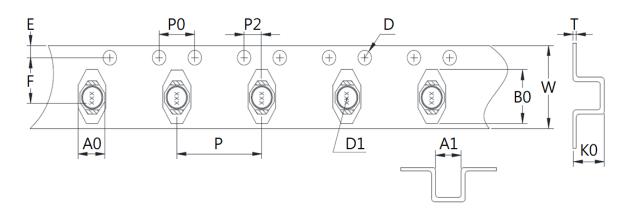
# 8. Packaging Information

# 8-1. Reel Dimension (Unit: mm)



Туре	А	В	С	D
13"x16mm	16.5 Ref	2.3 Ref	100.0 Ref	330.0 Ref

# 8-2. Tape Dimension (Unit: mm)



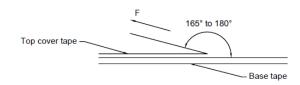
W	E	F	Р	P0	P2	D0
16.00±0.30	1.75±0.10	7.50±0.10	8.00±0.10	4.00±0.10	2.00±0.10	1.50±0.10
D1	Т	A0	A1	В0	K0	-
1.50±0.25	0.35±0.05	4.50±0.10	4.40±0.10	6.80±0.10	3.20±0.10	-



### 8-3. Packaging Type

Chip/Reel	2,500	
Inner Box	7,500	
Outer Box	15,000	

## 8-4. Tearing Off Force



The force for tearing off cover tape is according to the follow table, in the arrow direction under the following conditions.

(Referenced ANSI/EIA-481-D-2008 of 4.11 standard)

Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)	Tearing Speed (mm/min)
5~35	45~85	860~1060	300±10

Tape Size	8 mm	12 to 56 mm	72 mm or Wider
Tearing Off Force (grams)	10~100	10~130	10~150

## **Application Notice**

### 1. Storage Conditions

To maintain the solderability of terminal electrodes:

- (a) Recommended products should be used within 12 months from the time of delivery.
- (b) The packaging material should be kept where no chlorine or sulfur exists in the air.

### 2. Transportation

- (a) Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- (b) Vacuum pick up is strongly recommended for individual components.
- (c) Bulk handling should ensure that abrasion and mechanical shock are minimized.

