# 1. Part No. Expression

# PHC 0421 SPR10 M F

- (a)
- (b)
- (c)
- (d) (e) (f)
- (a) Series Code

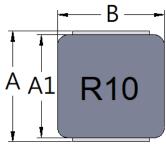
- (d) Inductance Code
- (b) Dimension Code

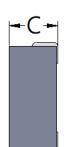
(e) Tolerance Code

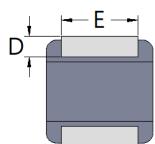
(c) Material Code

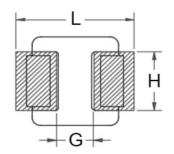
(f) Packaging Code

# 2. Configuration & Dimensions (Unit: mm)









Recommended PCB Layout

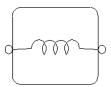
Note: 1. The above PCB layout reference only.

- 2. Recommend solder paste thickness at 0.12 mm and above.
- 3. Marking: Inductance Code, Black

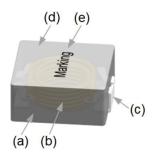
А	A1	В	С	D
4.3±0.3	4.1±0.3	4.2±0.2	1.9±0.2	0.8±0.3
E	L	G	Н	-
3.0±0.2	5.2 Ref	2.2 Ref	3.5 Ref	-



## 3. Schematic



## 4. Material List



NO	Items	
(a)	Core	
(b)	Wire	
(c)	Clip	
(d)	Paint	
(e)	Ink	

# 5. General Specifications

- (a) Operating Temp.: -40°C to +125°C (including self-temperature rise)
- (b) Storage Temp.: -40°C to +125°C (on board)
- (c) All test data referenced to 25°C ambient.
- (d) Heat Rated Current (Irms) will cause the coil temperature rise approximately  $\Delta T$  of 40°C.
- (e) Saturation Current (Isat) will cause inductance L0 to drop approximately 30%.
- (f) Rated DC Current: The lower value of Irms and Isat.
- (g) Part Temperature (Ambient + Temp. Rise): Should not exceed 125°C under worst case operating conditions.
- (h) Storage Condition (Component in its packaging)

i) Temperature: Less than 40°C

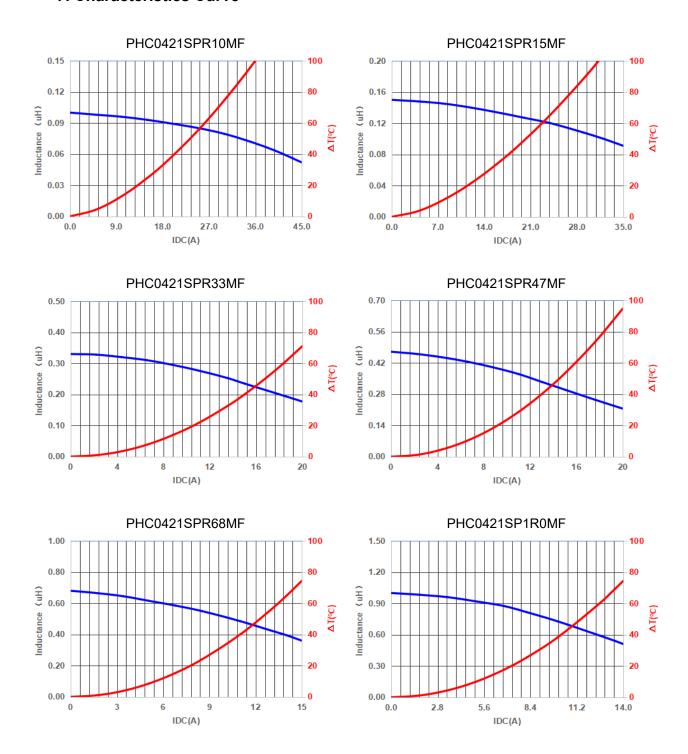
ii) Humidity: Less than 85% RH

## 6. Electrical Characteristics

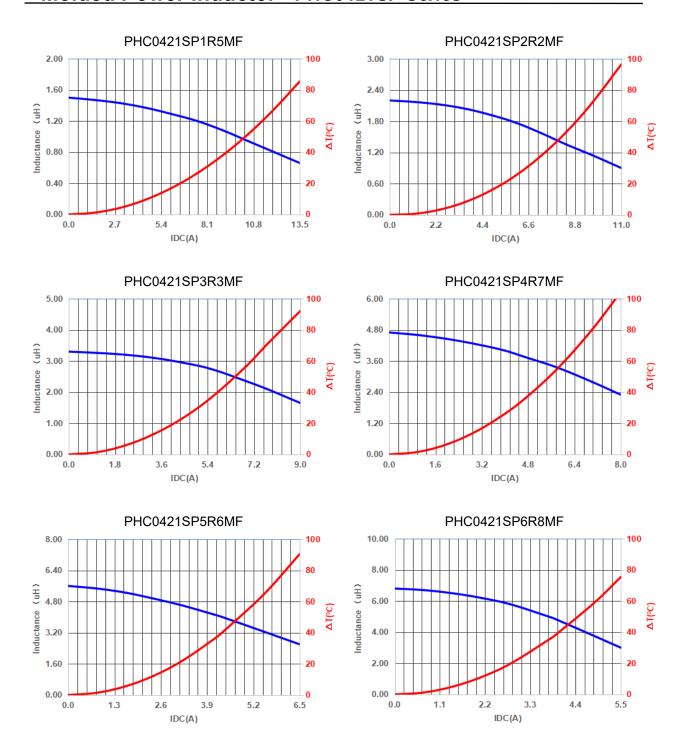
Part Number	Inductance (µH) @0A	Test	Irr (A	ns A)		at A)		CR iΩ)
	"±20%	Frequency	Тур	Max	Тур	Max	Тур	Max
PHC0421SPR10MF	0.10	1.0V/100KHz	19.0	18.0	35.0	32.0	1.9	2.3
PHC0421SPR15MF	0.15	1.0V/100KHz	16.5	15.0	29.0	26.0	3.1	3.8
PHC0421SPR33MF	0.33	1.0V/100KHz	15.0	13.0	15.0	13.0	5.0	5.8
PHC0421SPR47MF	0.47	1.0V/100KHz	13.0	11.0	13.0	11.0	6.0	7.2
PHC0421SPR68MF	0.68	1.0V/100KHz	11.0	10.0	11.0	10.0	8.2	9.9
PHC0421SP1R0MF	1.00	1.0V/100KHz	10.0	9.5	10.0	9.5	11.5	13.8
PHC0421SP1R5MF	1.50	1.0V/100KHz	9.0	8.0	9.0	8.0	15.4	18.5
PHC0421SP2R2MF	2.20	1.0V/100KHz	7.2	6.5	7.2	6.5	25.0	30.0
PHC0421SP3R3MF	3.30	1.0V/100KHz	5.5	5.0	6.9	6.2	41.0	49.2
PHC0421SP4R7MF	4.70	1.0V/100KHz	4.7	4.1	5.8	5.2	60.0	69.0
PHC0421SP5R6MF	5.60	1.0V/100KHz	4.1	3.5	4.3	3.7	68.0	78.2
PHC0421SP6R8MF	6.80	1.0V/100KHz	3.8	3.3	3.9	3.4	80.5	92.5
PHC0421SP8R2MF	8.20	1.0V/100KHz	3.3	3.0	3.5	3.1	105.0	121.0
PHC0421SP100MF	10.00	1.0V/100KHz	3.1	2.9	3.3	3.0	126.0	145.0



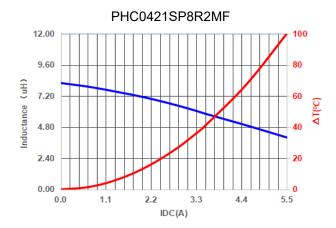
## 7. Characteristics Curve

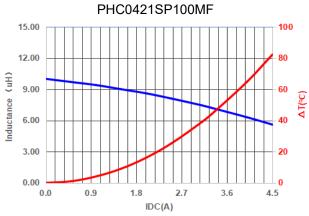












# 8. Soldering Specification

Mildly activated rosin fluxes are preferred. Our terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

## 8-1. IR Soldering Reflow

Recommended temperature profiles for lead free re-flow soldering in Figure 1, Table 1.1 & 1.2 (J-STD-020E).

#### 8-2. Iron Reflow

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended (Figure 2).

#### Note:

- (a) Preheat circuit and products to 150°C.
- (b) 355°C tip temperature (Max.)
- (c) Never contact the ceramic with the iron tip
- (d) 1.0mm tip diameter (Max.)
- (e) Use a 20 watt soldering iron with tip diameter of 1.0mm
- (f) Limit soldering time to 4~5 sec.

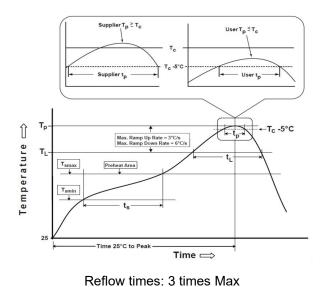
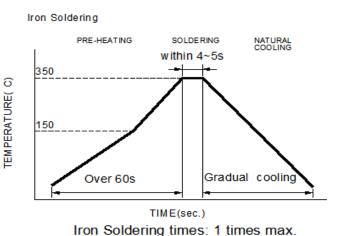


Figure 1: IR Soldering Reflow



Soldering iron method: 350±5°C Max

Figure 2: Iron soldering temperature profiles



Table (1.1) Reflow Profiles

Profile Type:	Pb-Free Assembly
Preheat	
-Temperature Min (T <sub>smin</sub> )	150°C
-Temperature Max (T <sub>smax</sub> )	200°C
-Time $(t_s)$ from $(T_{smin}$ to $T_{smax})$	60-120seconds
Ramp-up rate (T <sub>L</sub> to T <sub>p</sub> )	3°C /second max.
Liquids temperature (T <sub>L</sub> )	217°C
Time (t <sub>L</sub> ) maintained above T <sub>L</sub>	60-150 seconds
Classification temperature (Tc)	See Table (1.2)
Time (t <sub>p</sub> ) at Tc- 5°C (Tp should be equal to or less than Tc.)	*< 30 seconds
Ramp-down rate $(T_p \text{ to } T_L)$	6°C /second max.
Time 25°C to peak temperature	8 minutes max.

**Tp**: maximum peak package body temperature, **Tc**: the classification temperature.

For user (customer) **Tp** should be equal to or less than **Tc**.

Table (1.2) Package Thickness/Volume and Classification Temperature (T<sub>c</sub>)

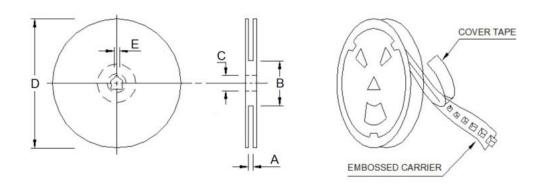
	Package	Volume mm <sup>3</sup>	Volume mm <sup>3</sup>	Volume
	Thickness	<350	350-2000	mm³ >2000
PB-Free	<1.6mm	260°C	260°C	260°C
	1.6-2.5mm	260°C	250°C	245°C
Assembly	≥2.5mm	250°C	245°C	245°C

Reflow is referred to standard IPC/JEDEC J-STD-020E.

<sup>\*</sup>Tolerance for peak profile temperature (Tp) is defined as a supplier minimum and a user maximum.

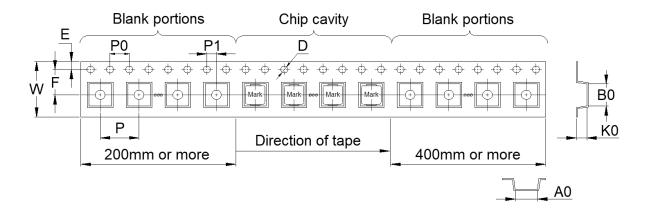
# 9. Packaging Information

## 9-1. Reel Dimension (Unit: mm)



Туре	А	В	С	D	E
13"x12mm	12.4+2.0/-0.0	100.0±2.0	13.0+0.5/-0.2	330.0±0.3	2.0±0.5

# 9-2. Tape Dimension (Unit: mm)



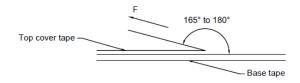
В0	A0	K0	W	Р
4.90±0.10	4.60±0.10	2.30±0.10	12.00±0.30	8.00±0.10
P0	P1	F	Е	D
4.00±0.10	2.00±0.10	5.50±0.10	1.75±0.10	1.50±0.10



## 9-3. Packaging Quantity (Unit: Pcs)

Chip/ Reel	3,000
Chip/ Reel	3,000

## 9-4. Tearing Off Force



The force for tearing off cover tape is according to the follow table, in the arrow direction under the following conditions.

(Referenced ANSI/EIA-481-D-2008 of 4.11 standard)

Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)	Tearing Speed (mm/min)
5~35	45~85	860~1060	300±10

Tape Size	8 mm	12 to 56 mm	72 mm or Wider
Tearing Off Force (grams)	10~100	10~130	10~150

# **Application Notice**

## 1. Storage Conditions

To maintain the solderability of terminal electrodes:

- (a) Recommended products should be used within 12 months from the time of delivery.
- (b) The packaging material should be kept where no chlorine or sulfur exists in the air.

## 2. Transportation

- (a) Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- (b) Vacuum pick up is strongly recommended for individual components.
- (c) Bulk handling should ensure that abrasion and mechanical shock are minimized.

