# 1. Part No. Expression

# <u>SPSQ8040 N R50 M</u>

- (a)
- (b)
- (c)
- d) (e)
- (a) Series Code

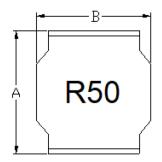
(d) Inductance Code

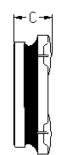
(b) Dimension Code

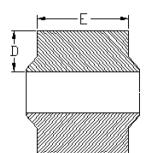
(e) Tolerance Code

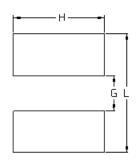
(c) Material Code

# 2. Configuration & Dimensions (Unit: mm)









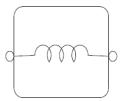
Recommended PCB Layout

Note: 1. The above PCB layout reference only.

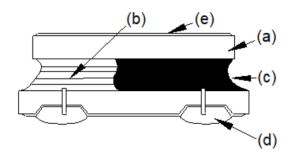
- 2. Recommend solder paste thickness at 0.15 mm and above.
- 3. A, B Size may slightly bigger than ferrite core dimension after epoxy sealing, but not exceed 0.1mm.
- 4. Component height may slightly higher than C size and not exceed 0.1 mm, and will be lower after reflow soldering.
- 5. Marking: Inductance Code, Black

Inductance	Α	В	С	D	Е	L	G	Н
<10 uH	9.0.0.3	0.0.0	4.2 Max		00.00	0.5 D-4	0.0 D-4	0 0 D - f
≥10 uH	8.0±0.3	8.0±0.3	3.7±0.3	2.4±0.3	6.3±0.3	8.5 Ref	2.8 Ref	6.6 Ref

### 3. Schematic



## 4. Material List

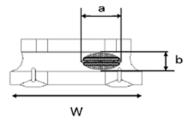


- (a) Core
- (b) Wire
- (c) Glue
- (d) Terminal
- (e) Ink

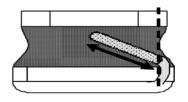
Void appearance tolerance limit & size of voids occurring to coating resin is specified below.

Exposed wire tolerance limit of coating resin part on product side.

Size of exposed wire occurring to coating resin is specified below:



- Width direction (dimension a): Acceptable when a ≤ w/2;
  Nonconforming when a>w/2
- 2. Length direction (dimension b): Dimension b is not specified
- 3. The total area of exposed wire occurring to each side is not greater than 50% of coating resin area and is acceptable



External appearance criterion for exposed wire.

Exposed end of the winding wire at the secondary side should be 2mm and below.



# 5. General Specifications

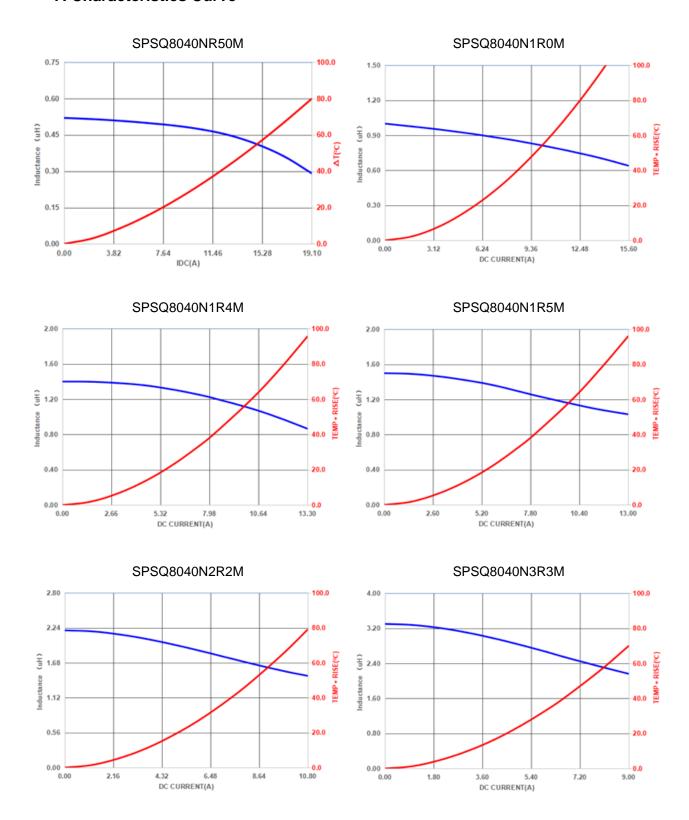
- (a) Reliability test for this part meets AEC-Q200 standard.
- (b) Operating Temp.: -55°C to +125°C (including self-temperature rise)
- (c) Storage Temp.: -55°C to +125°C (on board)
- (d) All test data referenced to 25°C ambient.
- (e) Heat Rated Current (Irms) will cause the coil temperature rise approximately  $\Delta T$  of 40°C.
- (f) Saturation Current (Isat) will cause inductance L0 to drop approximately 30%.
- (g) Part Temperature (Ambient + Temp. Rise): Should not exceed 125°C under worst case operating conditions.
- (h) Rated Current: The lower value of Isat and Irms.
- (i) Storage Condition (Component in its packaging)
  - i) Temperature: Less than 40°C
  - ii) Humidity: Less than 60% RH

## 6. Electrical Characteristics

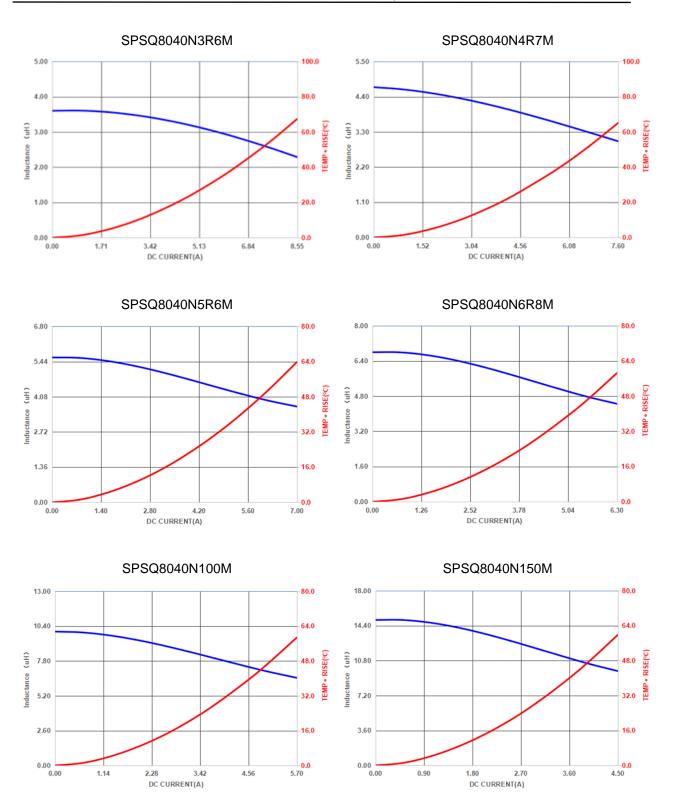
Part Number	Inductance Test (µH) @0A		Irms (A)		Isat (A)		DCR (mΩ)	
	±20%	Frequency	Тур	Max	Тур	Max	±20%	
SPSQ8040NR50M	0.50	1V/1MHz	12.00	10.00	17.00	15.00	5.5	
SPSQ8040N1R0M	1.00	1V/1MHz	8.50	8.00	13.80	13.00	8.2	
SPSQ8040N1R4M	1.40	1V/1MHz	8.20	7.80	11.80	11.20	10.0	
SPSQ8040N1R5M	1.50	1V/1MHz	8.00	7.70	11.50	11.00	10.0	
SPSQ8040N2R2M	2.20	1V/1MHz	7.40	6.90	9.80	9.20	11.5	
SPSQ8040N3R3M	3.30	1V/1MHz	6.60	6.20	8.00	7.50	15.0	
SPSQ8040N3R6M	3.60	1V/1MHz	6.40	6.00	7.60	7.00	15.0	
SPSQ8040N4R7M	4.70	1V/1MHz	5.80	5.30	6.70	6.00	19.5	
SPSQ8040N5R6M	5.60	1V/1MHz	5.40	5.20	6.20	5.80	22.0	
SPSQ8040N6R8M	6.80	1V/1MHz	5.10	5.00	5.60	5.10	25.0	
SPSQ8040N100M	10.0	1V/1MHz	4.60	4.20	5.00	4.30	33.0	
SPSQ8040N150M	15.0	1V/1MHz	3.60	3.20	4.00	3.60	50.0	
SPSQ8040N220M	22.0	1V/1MHz	2.90	2.45	3.10	2.80	73.0	
SPSQ8040N330M	33.0	1V/1MHz	2.30	2.10	2.60	2.10	100	
SPSQ8040N470M	47.0	1V/1MHz	2.00	1.70	2.20	1.90	135	
SPSQ8040N560M	56.0	1V/1MHz	1.75	1.60	1.90	1.60	160	
SPSQ8040N680M	68.0	1V/1MHz	1.65	1.50	1.75	1.50	205	
SPSQ8040N820M	82.0	1V/1MHz	1.40	1.30	1.60	1.40	230	
SPSQ8040N101M	100	1V/1MHz	1.20	1.10	1.45	1.20	300	
SPSQ8040N121M	120	1V/1MHz	1.10	1.00	1.30	1.10	350	
SPSQ8040N151M	150	1V/1MHz	0.98	0.90	1.20	1.03	410	
SPSQ8040N181M	180	1V/1MHz	0.91	0.83	1.04	0.94	490	
SPSQ8040N221M	220	1V/100MHz	0.85	0.76	0.99	0.90	610	
SPSQ8040N331M	330	1V/100MHz	0.70	0.66	0.75	0.70	850	
SPSQ8040N471M	470	1V/100MHz	0.63	0.58	0.60	0.55	1300	



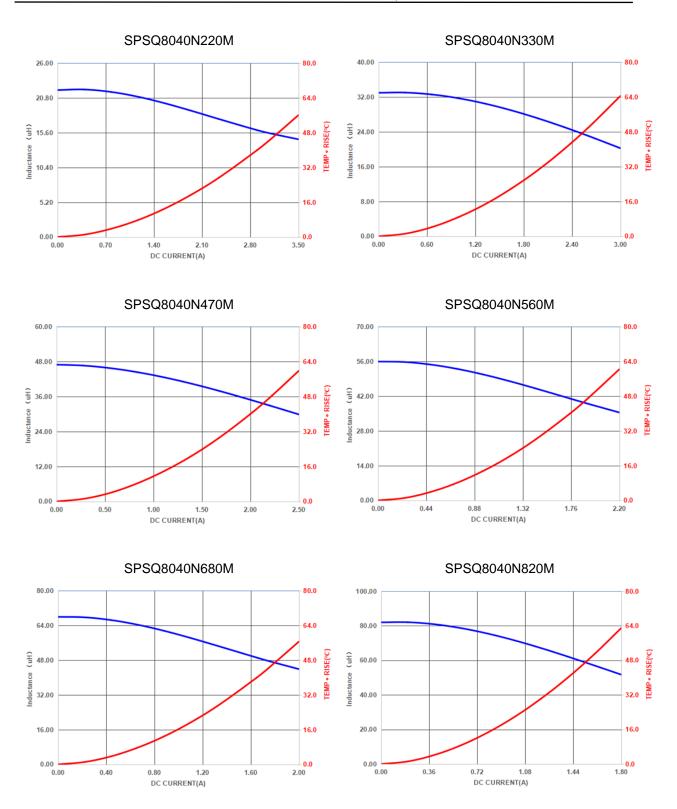
## 7. Characteristics Curve



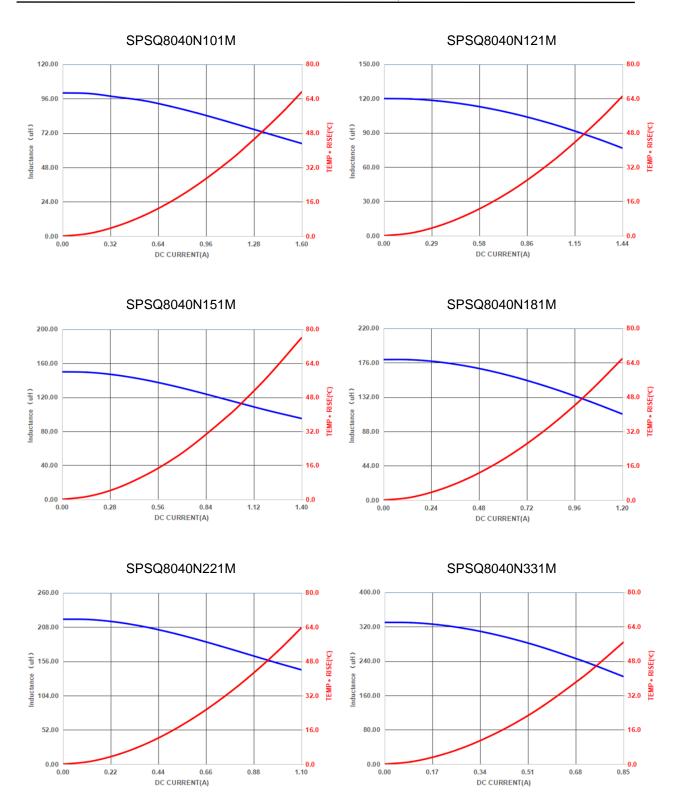




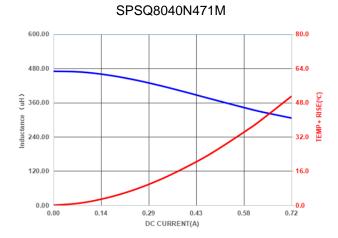












# 8. Soldering Specification

Mildly activated rosin fluxes are preferred. Our terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

### 8-1. IR Soldering Reflow

Recommended temperature profiles for lead free re-flow soldering in Figure 1, Table 1.1 & 1.2 (J-STD-020E).

#### 8-2. Iron Reflow

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended (Figure 2).

#### Note:

- (a) Preheat circuit and products to 150°C.
- (b) 355°C tip temperature (Max.)
- (c) Never contact the ceramic with the iron tip
- (d) 1.0mm tip diameter (Max.)
- (e) Use a 20 watt soldering iron with tip diameter of 1.0mm
- (f) Limit soldering time to 4~5 sec.

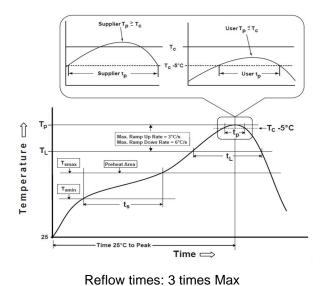
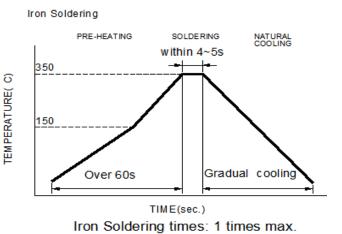


Figure 1: IR Soldering Reflow



Soldering iron method: 350±5°C Max

Figure 2: Iron soldering temperature profiles



Table (1.1) Reflow Profiles

Profile Type:	Pb-Free Assembly
Preheat	
-Temperature Min (T <sub>smin</sub> )	150°C
-Temperature Max (T <sub>smax</sub> )	200°C
-Time $(t_s)$ from $(T_{smin} \text{ to } T_{smax})$	60-120seconds
Ramp-up rate (T <sub>L</sub> to T <sub>p</sub> )	3°C /second max.
Liquids temperature (T <sub>L</sub> )	217°C
Time (t <sub>L</sub> ) maintained above T <sub>L</sub>	60-150 seconds
Classification temperature (T <sub>c</sub> )	See Table (1.2)
Time (t <sub>p</sub> ) at Tc- 5°C (Tp should be equal to or less than Tc.)	*< 30 seconds
Ramp-down rate (T <sub>p</sub> to T <sub>L</sub> )	6°C /second max.
Time 25°C to peak temperature	8 minutes max.

**Tp**: maximum peak package body temperature, **Tc**: the classification temperature.

For user (customer) **Tp** should be equal to or less than **Tc**.

Table (1.2) Package Thickness/Volume and Classification Temperature (T<sub>c</sub>)

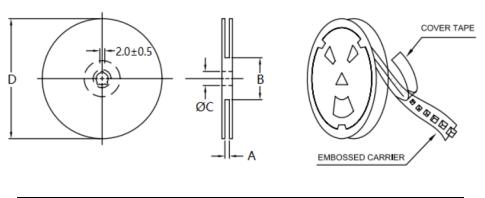
	Package	Volume mm <sup>3</sup>	Volume mm <sup>3</sup>	Volume
	Thickness	<350	350-2000	mm³ >2000
PB-Free	<1.6mm	260°C	260°C	260°C
	1.6-2.5mm	260°C	250°C	245°C
Assembly	≥2.5mm	250°C	245°C	245°C

Reflow is referred to standard IPC/JEDEC J-STD-020E.

<sup>\*</sup>Tolerance for peak profile temperature (Tp) is defined as a supplier minimum and a user maximum.

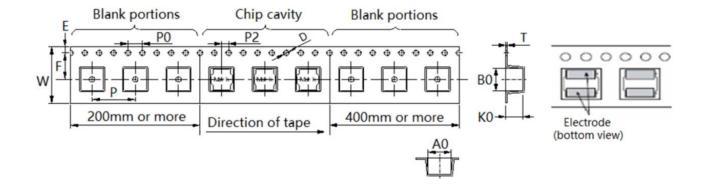
# 9. Packaging Information

## 9-1. Reel Dimension (Unit: mm)



Туре	А	В	С	D
13"x16mm	16.4+2.0/-0.0	80.0±2.0	13.0+0.5/-0.2	330.0±3.0

## 9-2. Tape Dimension (Unit: mm)

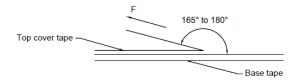


В0	A0	K0	Р	W	Т
8.40±0.10	8.40±0.10	4.30±0.10	12.00±0.10	16.00±0.30	0.40±0.10
Е	F	D	P0	P2	-
1.75±0.10	7.50±0.10	1.50±0.10	4.00±0.10	2.00±0.10	-



## 9-3. Packaging Quantity (Unit: Pcs)

## 9-4. Tearing Off Force



The force for tearing off cover tape is according to the follow table, in the arrow direction under the following conditions.

(Referenced ANSI/EIA-481-D-2008 of 4.11 standard)

Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)	Tearing Speed (mm/min)
5~35	45~85	860~1060	300±10

Tape Size	8 mm	12 to 56 mm	72 mm or Wider
Tearing Off Force (grams)	10~100	10~130	10~150

# **Application Notice**

### 1. Storage Conditions

To maintain the solderability of terminal electrodes:

- (a) Recommended products should be used within 12 months from the time of delivery.
- (b) The packaging material should be kept where no chlorine or sulfur exists in the air.

## 2. Transportation

- (a) Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- (b) Vacuum pick up is strongly recommended for individual components.
- (c) Bulk handling should ensure that abrasion and mechanical shock are minimized.

