### 1. Part No. Expression

# **SPS** 201610 E R12 M F

- (a)
- (b)
- (c) (d) (e) (f)
- (a) Series Code

(d) Inductance Code

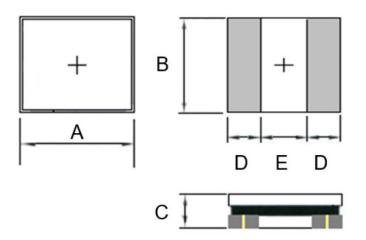
**Dimension Code** 

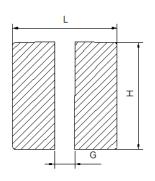
**Tolerance Code** 

(c) Material Code

Packaging Code

## 2. Configuration & Dimensions (Unit: mm)





Recommended PCB Layout

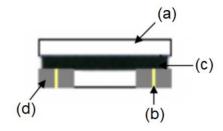
Note: 1. The above PCB layout reference only.

А	В	С	D
2.00+0.20/-0.10	1.60+0.20/-0.10	1.00 Max	0.60 Ref
E	L	G	Н
0.80 Ref	2.30 Ref	0.70 Ref	1.70 Ref

#### 3. Schematic



#### 4. Material List



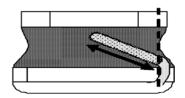
- (a) Core
- (b) Wire
- (c) Glue
- (d) Terminal

Void appearance tolerance limit & size of voids occurring to coating resin is specified below.

a b W Exposed wire tolerance limit of coating resin part on product side.

Size of exposed wire occurring to coating resin is specified below:

- Width direction (dimension a) : Acceptable when a ≤ w/2;
  Nonconforming when a>w/2
- 2. Length direction (dimension b): Dimension b is not specified
- The total area of exposed wire occurring to each side is not greater than 50% of coating resin area and is acceptable



External appearance criterion for exposed wire.

Exposed end of the winding wire at the secondary side should be 2mm and below.

### 5. General Specifications

(a) Operating Temp.: -40°C to +125°C (including self-temperature rise)

(b) Storage Temp.: -40°C to +125°C (on board)

(c) All test data referenced to 25°C ambient.

(d) Heat Rated Current (Irms) will cause the coil temperature rise ΔT of 40°CMax

(e) Saturation Current (Isat) will cause inductance L0 to drop approximately 30%.

(f) Storage Condition (Component in its packaging)

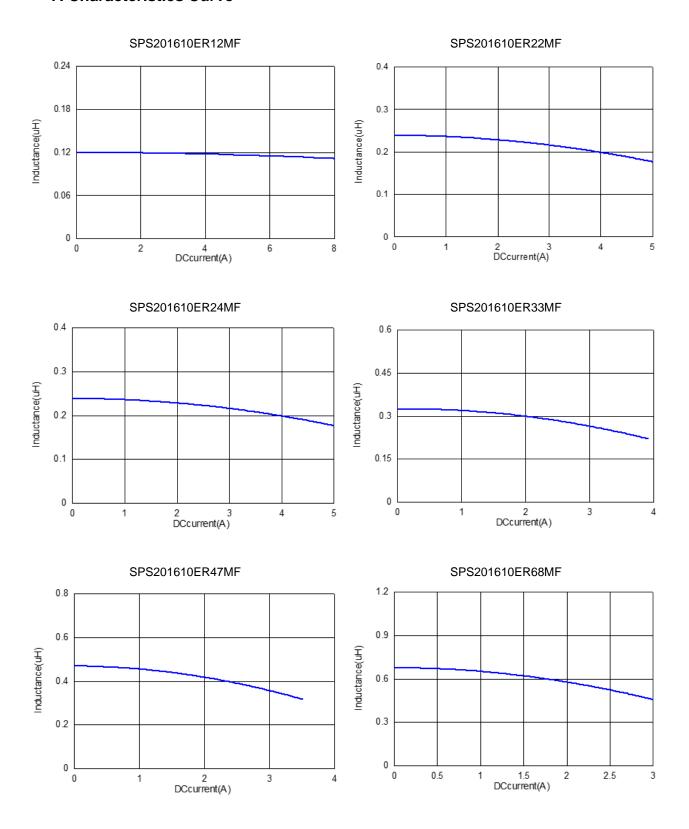
i) Temperature: Less than 40°Cii) Humidity: Less than 60% RH

#### 6. Electrical Characteristics

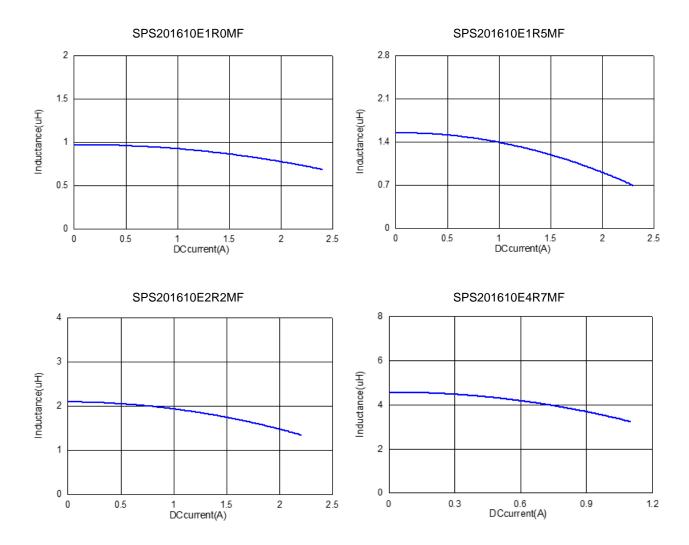
Part Number	Inductance (µH) @0A	Test		ns A)		at A)	) ()	CR 2)
raitivambor	±20%	Frequency	Тур	Max	Тур	Max	Тур	Max
SPS201610ER12MF	0.12	0.1V/1MHz	5.00	4.50	7.00	6.00	0.021	0.026
SPS201610ER22MF	0.22	0.1V/1MHz	4.40	3.90	5.10	4.50	0.023	0.028
SPS201610ER24MF	0.24	0.1V/1MHz	4.40	3.90	5.10	4.50	0.023	0.028
SPS201610ER33MF	0.33	0.1V/1MHz	3.50	3.10	3.90	3.50	0.031	0.040
SPS201610ER47MF	0.47	0.1V/1MHz	3.30	3.00	3.85	3.40	0.035	0.042
SPS201610ER68MF	0.68	0.1V/1MHz	2.80	2.50	3.25	2.80	0.046	0.055
SPS201610E1R0MF	1.00	0.1V/1MHz	2.40	2.20	2.90	2.50	0.059	0.072
SPS201610E1R5MF	1.50	0.1V/1MHz	2.10	1.80	2.30	1.80	0.098	0.118
SPS201610E2R2MF	2.20	0.1V/1MHz	1.70	1.55	2.10	1.70	0.141	0.170
SPS201610E4R7MF	4.7	0.1V/1MHz	1.00	0.90	1.00	0.90	0.320	0.384



### 7. Characteristics Curve







### 1. Soldering Specification

Mildly activated rosin fluxes are preferred. Our terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

#### 1-1. IR Soldering Reflow

Recommended temperature profiles for lead free re-flow soldering in Figure 1, Table 1.1 & 1.2 (J-STD-020E).

#### 1-2. Iron Reflow

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended (Figure 2).

#### Note:

- (a) Preheat circuit and products to 150°C.
- (b) 355°C tip temperature (Max.)
- (c) Never contact the ceramic with the iron tip
- (d) 1.0mm tip diameter (Max.)
- (e) Use a 20 watt soldering iron with tip diameter of 1.0mm
- (f) Limit soldering time to 4~5 sec.

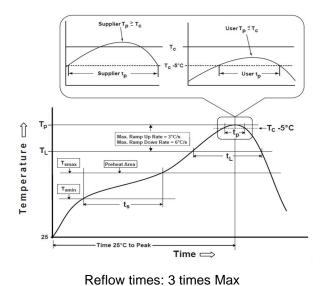
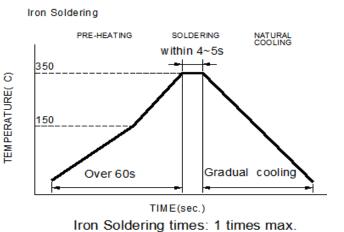


Figure 1: IR Soldering Reflow



Soldering iron method: 350±5°C Max

Figure 2: Iron soldering temperature profiles



**Table (1.1) Reflow Profiles** 

Profile Type:	Pb-Free Assembly
Preheat	
-Temperature Min (T <sub>smin</sub> )	150°C
-Temperature Max (T <sub>smax</sub> )	200°C
-Time $(t_s)$ from $(T_{smin} \text{ to } T_{smax})$	60-120seconds
Ramp-up rate (T <sub>L</sub> to T <sub>p</sub> )	3°C /second max.
Liquids temperature (T <sub>L</sub> )	217°C
Time (t <sub>L</sub> ) maintained above T <sub>L</sub>	60-150 seconds
Classification temperature (Tc)	See Table (1.2)
Time (t <sub>p</sub> ) at Tc- 5°C (Tp should be equal to or less than Tc.)	*< 30 seconds
Ramp-down rate (T <sub>p</sub> to T <sub>L</sub> )	6°C /second max.
Time 25°C to peak temperature	8 minutes max.

**Tp**: maximum peak package body temperature, **Tc**: the classification temperature.

For user (customer) **Tp** should be equal to or less than **Tc**.

Table (1.2) Package Thickness/Volume and Classification Temperature (T<sub>c</sub>)

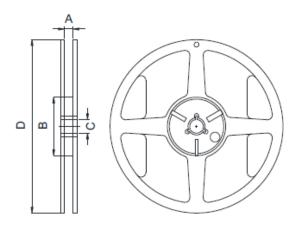
	Package	Volume mm <sup>3</sup>	Volume mm <sup>3</sup>	Volume
	Thickness	<350	350-2000	mm³ >2000
PB-Free	<1.6mm	260°C	260°C	260°C
	1.6-2.5mm	260°C	250°C	245°C
Assembly	≥2.5mm	250°C	245°C	245°C

Reflow is referred to standard IPC/JEDEC J-STD-020E.

<sup>\*</sup>Tolerance for peak profile temperature (Tp) is defined as a supplier minimum and a user maximum.

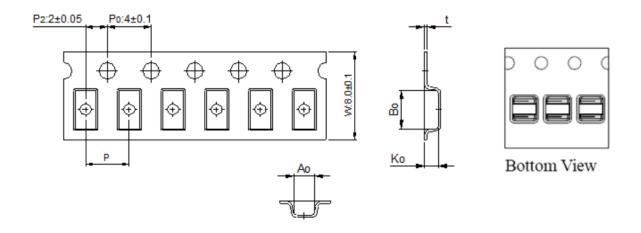
### 2. Packaging Information

### 2-1. Reel Dimension (Unit: mm)



Туре	А	В	С	D
7"x8 mm	8.4±1.0	50.0 Min	13.0±0.8	178.0±2.0

### 2-2. Tape Dimension (Unit: mm)



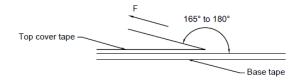
Ao	Во	Ko	Р	t
2.00±0.10	2.50±0.10	1.40±0.10	4.00±0.10	0.23±0.05



#### 2-3. Packaging Quantity (Unit: Pcs)

Chip/ Reel	2,000
------------	-------

#### 2-4. Tearing Off Force



The force for tearing off cover tape is according to the follow table, in the arrow direction under the following conditions.

(Referenced ANSI/EIA-481-D-2008 of 4.11 standard)

Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)	Tearing Speed (mm/min)
5~35	45~85	860~1060	300±10

Tape Size	8 mm	12 to 56 mm	72 mm or Wider
Tearing Off Force (grams)	10~100	10~130	10~150

### **Application Notice**

#### 1. Storage Conditions

To maintain the solderability of terminal electrodes:

- (a) Recommended products should be used within 12 months from the time of delivery.
- (b) The packaging material should be kept where no chlorine or sulfur exists in the air.

#### 2. Transportation

- (a) Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- (b) Vacuum pick up is strongly recommended for individual components.
- (c) Bulk handling should ensure that abrasion and mechanical shock are minimized.

